

Event Agenda



THURSDAY, FEBRUARY 4

Time (CST)	Session	Track	Presenter(s)
8:30 a.m. - 8:55 a.m.	Keynote		Rod Cope
9:00 a.m. - 9:50 a.m.	Scrum, Kanban, and Gantt Scheduling in One Project – Nonsense or Necessity?	Plan	Johan Karlsson
9:00 a.m. - 9:50 a.m.	Optimizing Your CI Pipeline	Create	Robert Cowham
9:00 a.m. - 9:50 a.m.	Embedded Security through Coding Standards and Static Analysis	Verify	Dr David Long – Doulos
10:00 a.m. - 10:50 a.m.	Planning & Tracking Without Jira – Can it Be Done?	Plan	Johan Karlsson
10:00 a.m. - 10:50 a.m.	Implementing a Unified HW/SW BoM to Reduce System Development	Create	Vishal Moondhra
10:00 a.m. - 10:50 a.m.	CI Efficiency: How to Verify Every Developer Commit – Even for Very Large Codebases – Without Quantum Computing or a Working Flux Capacitor	Verify	Steve Howard
11:00 a.m. - 11:50 a.m.	Community vs. Enterprise Open Source – Which is Right for Your Business?	Plan	Justin Reock
11:00 a.m. - 11:50 a.m.	Embedded Software Development at 5G Speed	Create	Hang Dong – Qualcomm
11:00 a.m. - 11:50 a.m.	Role of Coding Standards in Autonomous Vehicles	Verify	Jill Britton and Frank van den Beuken
12:00 p.m. - 12:50 p.m.	CWE Top 25: History, Tools and Techniques	Plan	Andrew Bedford
12:00 p.m. - 12:50 p.m.	Secure Collaboration in a Cloud-based Chip Design Environment	Create	Warren Savage
12:00 p.m. - 12:50 p.m.	How to Improve Development with Strong Requirements Taxonomy	Verify	Tom Totenberg
12:55 p.m. - 1:15 p.m.	Closing		Rod Cope

Agenda subject to change.