

DATASHEET

Perforce Planning and Collaboration Suite for Semiconductor Design

Unite Designers, Engineers, and Software Developers

Perforce offers a unique solution for IP and data management, trusted by 9/10 world's largest semiconductor companies.

With Perforce, designs and their accompanying software stack are more efficient, predictable, and higher quality, helping you meet time-to-market demands and boost reuse. Advanced IP release flows, cataloging, metadata, and workspace management ensure that no matter where designers are located, they have full visibility into the IP available to them and can easily access the corresponding data.










IP MANAGEMENT

[Methodics IPLM](#) is a comprehensive IP lifecycle management (IPLM) platform. Companies of all sizes can have complete control over the design and integration of both internal and external design elements. This includes libraries, analog and digital designs, standalone IP, and 3rd party IP. With features like Geofencing and Rules Engine, teams can set up IP to always be protected.

DATA MANAGEMENT

[Helix Core](#) is a high-performance data management solution that can be used to manage all digital assets — including binaries and Git source code. Teams can version everything for analog and digital development, plus scale infrastructure to support remote teams. Helix Core also protects assets down to the individual file.

Scale for Global Development

Global development teams benefit from Perforce's ability to securely manage all digital content — even large files — in a single repository. With a robust caching and replication architecture, access for engineers and designers is seamless and instantaneous — no matter where they are in the world. Teams can securely code and IP across teams and products.

Get to 100% Reusability

Acceleration requires reuse. Learn how Perforce can help with...

DIGITAL DESIGNS

Get have control over the design and integration of both internal and external design elements, including libraries, new analog and digital designs, and standalone IPs.

Perforce Planning & Collaboration Suite for Semiconductor Design supports advanced tracking and management across multiple projects, multiple sites, and multiple generations of the IP's evolution. This gives hardware and software teams complete visibility into the state and usage of IP from the moment it enters the system until an SoC is delivered.

Perforce also manages the complex file relationships found in semiconductor designs. It handles the the large binary files and millions of files that are common in EDA tools. You get robust, federated multisite data management with complete disaster recovery capabilities.

ANALOG DESIGNS

Gain a comprehensive, united, and reliable design data management experience. VersIC integrates with Perforce from within the Cadence Virtuoso environment to deliver a truly collaborative design environment across global design locations. You can have one platform to control, manage, version, and release Cadence libraries, Verilog, scripts, software, docs, and other design assets.

Multiple Data Management Solutions

Perforce integrates with the tools teams use. That's why you can manage Git source code within Perforce data management. Git users can use their familiar interface, without having the security risk of files on their individual workstations. Plus, because Perforce manages binaries, you still have a single source of truth across your organization.

Increase Reuse and Decreases Friction

DYNAMIC AND INTEGRATED ENVIRONMENT

IPs can evolve according to their own lifecycle. With Perforce, you can build a searchable IP catalog the includes information about bugs, tests, and more. Plus, it integrates with the tools your teams already use.

WORKSPACE AND RELEASE MANAGEMENT

Advanced tracking of the IP release flows and global workspace management increases visibility, helping developers and designs fully understand the context of an IP for inclusion into their project.

IP-CENTRIC APPROACH

Perforce is low maintenance with relatively few insertion points into the user workflow. It enhances your existing design environment without disrupting your PLM tool.

SUPERIOR PLANNING

Start every project out right. The Component & IP BoM (CIPB) Planning Module works with your ERM and manufacturing focused PLM tool to provide high level data on IP that matches your requirements. Plus, deliver software and hardware in a single, unified BoM to manufacturing.

Start Your Journey

Contact our IP experts to learn more. We even have promotional pricing for startups looking to uplevel to enterprise tools.

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